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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/629,085	
	Filing Date	July 31, 2000	
	First Named Inventor	Peter C. Damron	
	Group Art Unit	2751	
	Examiner Name	(to be assigned)	
Total Number of Pages in This Submission	10	Attorney Docket Number	SUN-P4935

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



APPLICANT: Peter C. Damron

SERIAL NO.: 09/629,085

FILING DATE: July 31, 2000

TITLE: METHOD AND SYSTEM FOR TRANSLATION LOOKASIDE
BUFFER COHERENCE IN MULTI-PROCESSOR SYSTEMS

EXAMINER: (to be assigned)

ART UNIT: 2751

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PRELIMINARY AMENDMENT

Please amend the above-identified application as follows.

In the Claims:

Please amend claims 1, 8, 9 and 12-18 as follows:

- Sub B4
A1
1. (Once Amended) A method for maintaining translation lookaside buffer ("TLB") coherency in a computer system having a plurality of processors, each of said processors having an associated TLB for storing an address translation data, the system having a main communication network coupled to the plurality of processors, said method comprising: